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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2005/0278670	12-2005	Brooks et al.	716/005
*	B	US-2003/0079010	04-2003	Osborn, Andrew R.	709/224
*	C	US-2002/0138817	09-2002	Lee et al.	716/12
*	D	US-2002/0078421	06-2002	Bradley et al.	716/1
*	E	US-6,957,413	10-2005	McKeone et al.	716/18
*	F	US-6,948,145	09-2005	Brown et al.	716/12
*	G	US-6,907,583	06-2005	Abt et al.	716/1
*	H	US-6,851,094	02-2005	Robertson et al.	716/1
*	I	US-6,675,366	01-2004	Nolan, Stephen David	716/11
*	J	US-6,615,400	09-2003	Lukanc, Todd P.	716/11
*	K	US-6,609,227	08-2003	Bradley et al.	716/1
*	L	US-6,546,536	04-2003	Nolan, Stephen David	716/11
*	M	US-6,539,533	03-2003	Brown et al.	716/17

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Yen et al., "A Web-Based, Collaborative, Computer-Aided Sequential Control Design Tool", IEEE Control Systems Magazine, Vol. 23, No. 2, April 2003, pp. 14-19.			
	V	Dalpasso et al., "Specification and Validation of Distributed IP-Based Designs with JavaCAD", Proceedings of Design, Automation and Test in Europe Conference and Exhibition, 9 March 1999, pp. 684-688.			
	W	Lee et al., "CADIC: Computer-Aided Design on Internet with Cryptosystem", 1998 IEEE International Conference on Systems, Man, and Cybernetics, Vol. 3, 11 October 1998, pp. 2670-2674.			
	X	Rao et al., "Web-Based Network Analysis and Design", ACM Transactions on Modeling and Computer Simulation, Vol. 10, No. 1, January 2000, pp. 18-38.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,502,229	12-2002	Lee et al.	716/12
*	B	US-6,449,762	09-2002	McElvain, Kenneth S.	716/18
*	C	US-6,401,230	06-2002	Ahanessians et al.	716/1
*	D	US-6,230,305	05-2001	Meares, Lawrence G.	716/11
*	E	US-6,115,034	09-2000	Tanaka et al.	715/700
*	F	US-5,883,807	03-1999	Fanjoy, Logan M.	716/8
*	G	US-5,673,198	09-1997	Lawman et al.	716/11
*	H	US-5,526,517	06-1996	Jones et al.	707/8
*	I	US-2003/0121019	06-2003	Brown et al.	716/12
*	J	US-2004/0066922	04-2004	Castro et al.	379/115.01
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Benini et al., "Distributed EDA Tool Integration: the PPP Paradigm", 1996 IEEE International Conference on Computer Design: VLSI in Computers and Processors, 7 October 1996, pp. 448-453.			
	V	Chan et al., "WELD--an Environment for Web-based Electronic Design", Proceedings of 1998 Design Automation Conference, 15 June 1998, pp. 146-151.			
	W	Saha et al., "Web-based Distributed VLSI Design", 1998 Proceedings of the Eleventh International Conference on VLSI Design, 4 January 1998, pp. 449-454.			
	X	Wirthlin et al., "IP Delivery for FPGAs Using Applets and JHDL", 2002 Proceedings of 39th Design Automation Conference, 10 June 2002, pp. 2-7.			

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/603,493	Applicant(s)/Patent Under Reexamination PERRY ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Marinova et al., "A Distributed System for Circuit Simulation", 2001 TELSIS 5th International Conference on Telecommunications in Modern Satellite, Cable and Broadcasting Service, Vol. 2, 19 September 2001, pp. 467-470.
	V	Brglez et al., "A Universal Client for Distributed Networked Design and Computing", DAC, 18 June 2001, pp. 401-406.
	W	Petrenko, "Teleworking in Computer-Aided Circuit Design", CADSM 2003, 18 February 2003, pp. 25-29.
	X	Drofenik et al., "Interactive Power Electronics Seminar IIPES)-a Web-based Introductory Power Electronics Course Employing Java-Applets", 2002 IEEE 33rd Annual Power Electronics Specialists Conference, Vol. 2, 23 June 2002, pp. 443-448.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/603,493	Applicant(s)/Patent Under Reexamination PERRY ET AL.	
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	A	US-			
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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Geppert, "IC Design on the World Wide Web", IEEE Spectrum, Vol. 35, No. 6, June 1998, pp. 45-50.
	V	Lin, "Computing Brokerage and Its Application in VLSI Design", Proceedings of the ASP-DAC '97, Asia and South Pacific Design Automation Conference, 28 January 1997, pp. 65-69.
	W	
	X	

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Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.